File  View  Edit  Tools  Window  Help

- Drafts
- Pending
- Active
  - L1: (56938) (semiconductor or integrated adj circuit$1) and (i/o or input$1/output$1)
  - L6: (17728) 1 and (driver$1 or weak adj driver$1)
  - L11: (109) 6 and (test$4 adj fixtur$4)
  - L16: (50) 11 and test$4 adj signal$1
  - L21: (0) 16 and (connect$4 near faul$4)
  - L26: (37) 16 and (faul$4 or fail$4)
  - L31: (3) 1 and (reduc$4 adj pin near test$4)
  - L36: (285) 1 and (reduc$4 adj pin$1 or reduc$4-pin$1)
  - L41: (126) 36 and (driver$1 or weak adj driver$1)
- Failed
- Saved
- Favorites
- Tagged (4)
- UDC
- Queue
- Trash

Search   List   Browse   Queue   Clear

DBs: USPAT;EPO;JPO;DERWENT          ☐ Plurals

Default operator: OR          ☑ Highlight all hit terms initially

36 and (driver$1 or weak adj driver$1)

BRS terms    IS&R form    Image    Text    HTML

| | U | 1 | Document ID | Issue Date | Pages | Title | Current OR | Current XRe | Retrieval | Inventor | S | C | P | 2 | 3 | | 5 | I |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 1 | ☐ | ☐ | US 6513134 B1 | 20030128 | 17 | System and method for tracing program execution | 714/38 | 712/227; 717/127; | | Augsburg, Victor Roberts et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 2 | ☐ | ☐ | US 6510503 B2 | 20030121 | 31 | High bandwidth memory interface | 711/167 | 711/168; 711/169; | | Gillingham, Peter et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 3 | ☐ | ☐ | US 6504754 B2 | 20030107 | 69 | Array architecture and operating methods for dig | 365/185.03 | 365/185.18; 365/185.19 | | Tran, Hieu Van et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 4 | ☐ | ☐ | US 6496187 B1 | 20021217 | 45 | Graphics system configured to perform par | 345/419 | 345/611 | | Deering, Michael F. et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 5 | ☐ | ☐ | US 6496186 B1 | 20021217 | 43 | Graphics system having a super-sampled sample buff | 345/419 | 345/611 | | Deering, Michael F. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 6 | ☐ | ☐ | US 6489956 B1 | 20021203 | 42 | Graphics system having a super-sampled sample buff | 345/419 | 345/611 | | Deering, Michael F. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 7 | ☐ | ☐ | US 6483504 B1 | 20021119 | 39 | Graphics system having a super sampled-sample buff | 345/419 | | | Deering, Michael F. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 8 | ☐ | ☐ | US 6466206 B1 | 20021015 | 42 | Graphics system with programmable real-time al | 345/419 | | | Deering, Michael F. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 9 | ☐ | ☐ | US 6462600 B2 | 20021008 | 14 | Read-head preamplifier having internal offset co | 327/307 | 330/259; 360/67 | | Pakriswamy, Elango | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 10 | ☐ | ☐ | US 6459428 B1 | 20021001 | 36 | Programmable sample filtering for image rende | 345/419 | | | Burk, Wayne Eric et al. | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |
| 11 | ☐ | ☐ | US 6436725 B2 | 20020820 | 48 | Method of manufacturing semiconductor device usin | 438/14 | 438/18 | | Sakata, Takeshi | ☑ | ☐ | ☐ | ☐ | ☐ | ☐ | ☐ | U: |

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